

<b>Notice of References Cited</b>	Application/Control No. 10/632,195	Applicant(s)/Patent Under Reexamination LIN ET AL.	
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,272,610	08-2001	Katayama et al.	711/171
	B	US-6,877,080	04-2005	Roohparvar, Frankie F.	711/167
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Austin et al., "Zero-Cycle Loads: Microarchitecture Support ofr Reducing Load Latency", © 1995 IEEE, p. 82-92.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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